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Cadence OrCAD PCB Designer Version 16.3 Professor John H. Davies Department of Electronics and Electrical Engineering Glasgow University, Glasgow, G12 8QQ, Scotland, UK Email: jdavies@elec.gla.ac.uk 2011 October 6 Contents Preamble 2 1 Introduction 3 2 One-transistor amplifier: Schematic capture 5 3

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